

Please replace the abstract with the following amended abstract:

The voltage level shifting circuit includes a complimentary signal input circuit having a pair of MOSs of a first withstand voltage and a load circuit having a pair of MOSs of a second withstand voltage, a first voltage down-converting element which prevents a potential level exceeding the first withstand voltage from being supplied supplying to a complementary signal input circuit, and a third MOS of the second withstand voltage electrically connecting a third power supply node to an output node ~~in response~~ responsive to a voltage potential from the load circuit. The voltage level shifting circuit also includes a fourth MOS of the first withstand voltage electrically connecting a first power supply node to the output node responsive ~~in response~~ to one of the voltage potentials of the complimentary signal, and a second voltage down-converting element which prevents a potential level exceeding the first withstand voltage from being supplied supplying to the fourth MOS.